

FIG. 1A

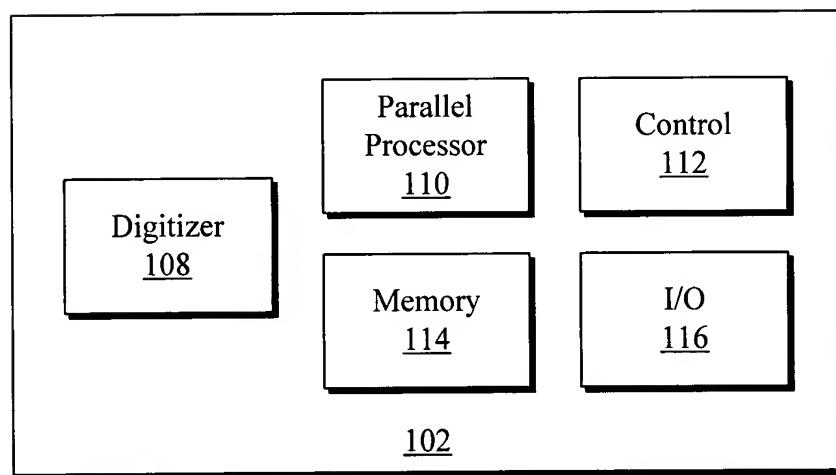


FIG. 1B

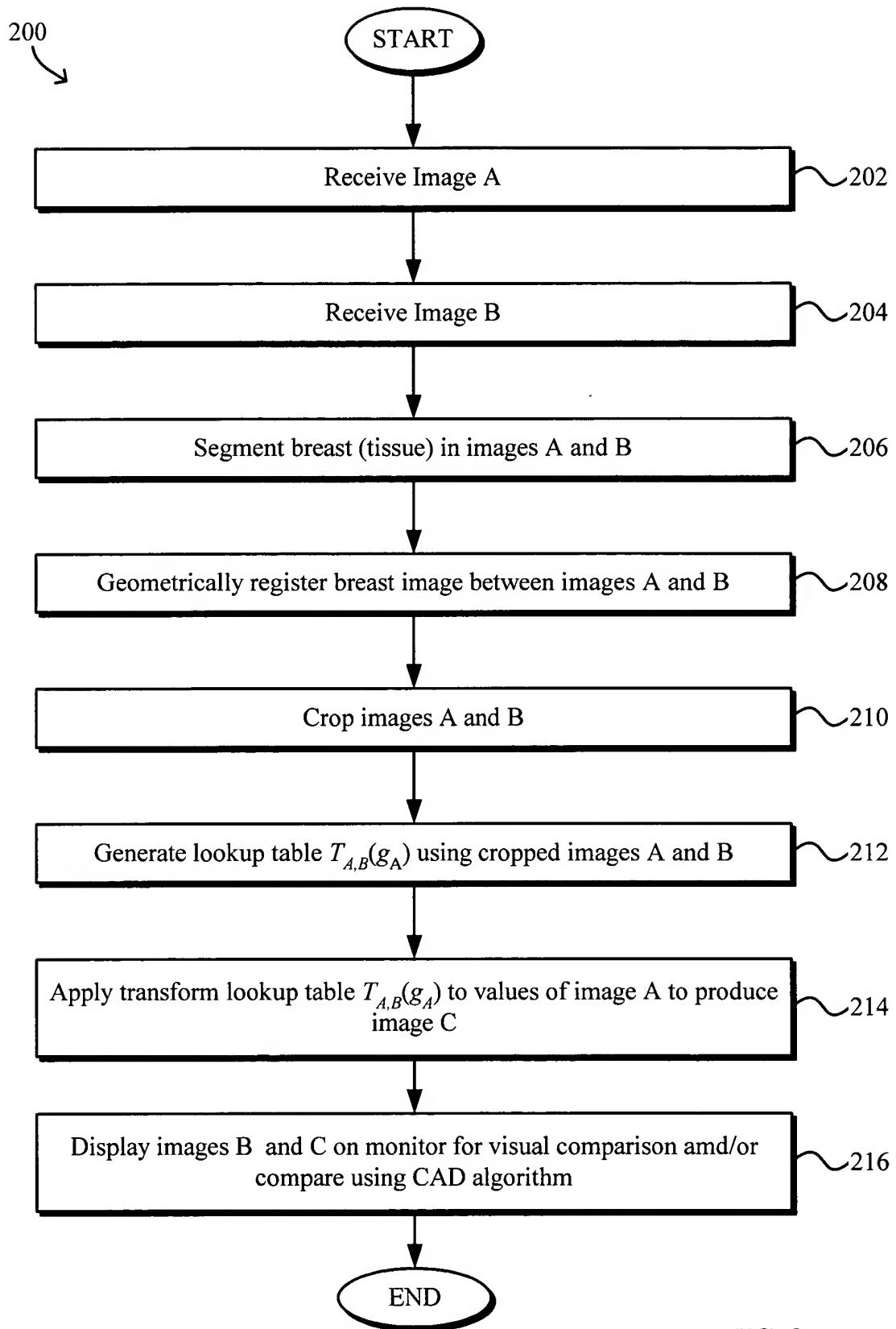


FIG. 2

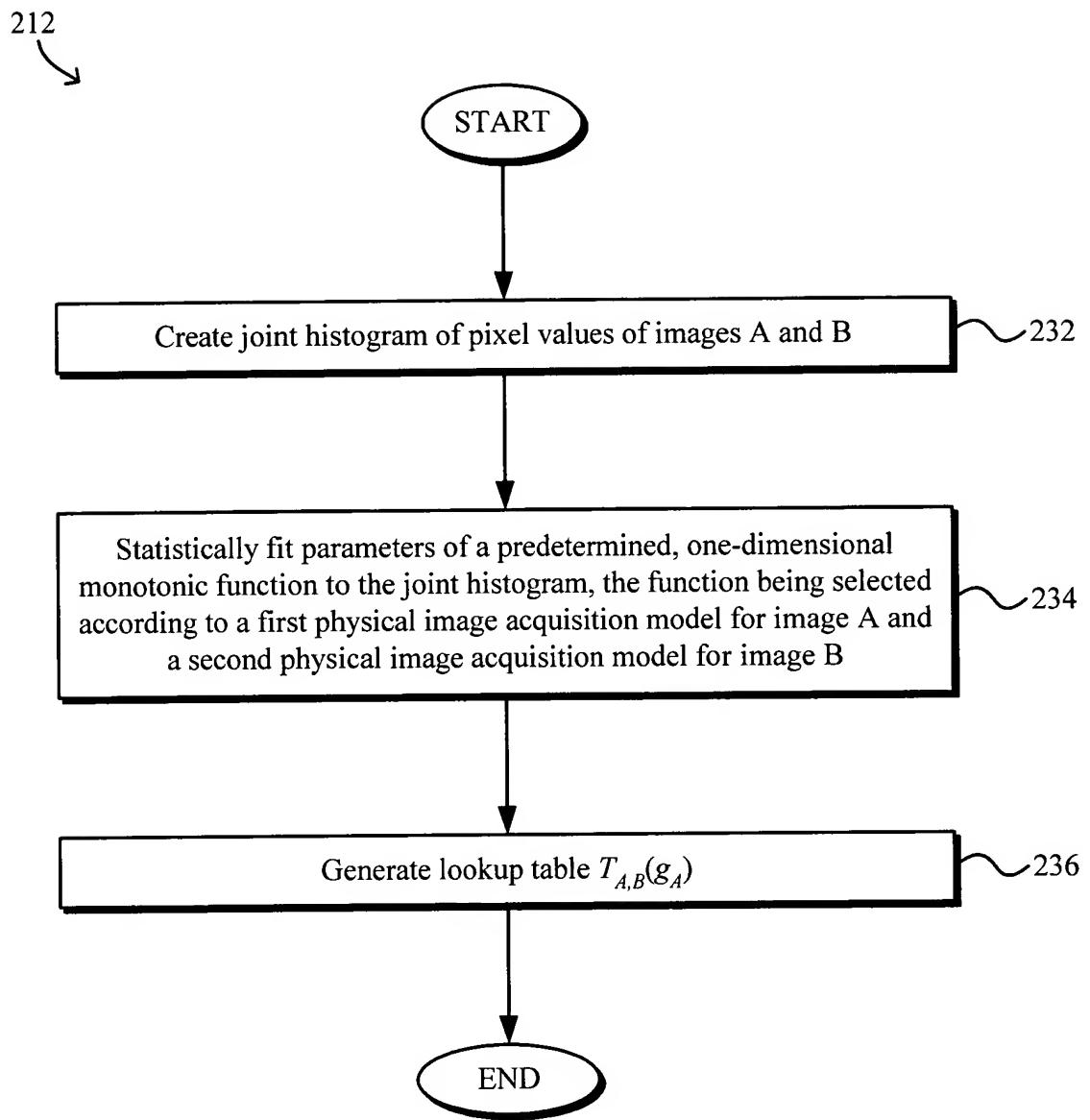


FIG. 3

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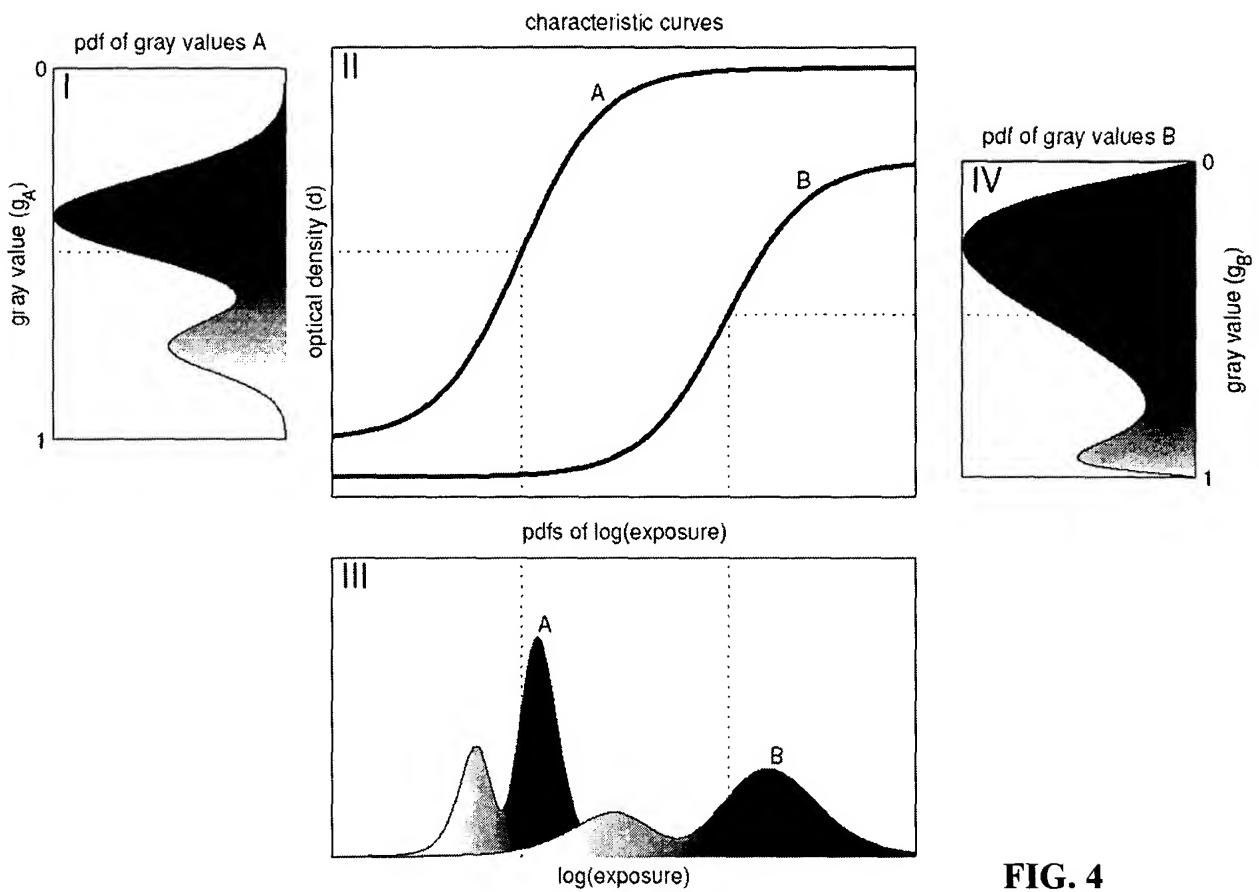


FIG. 4

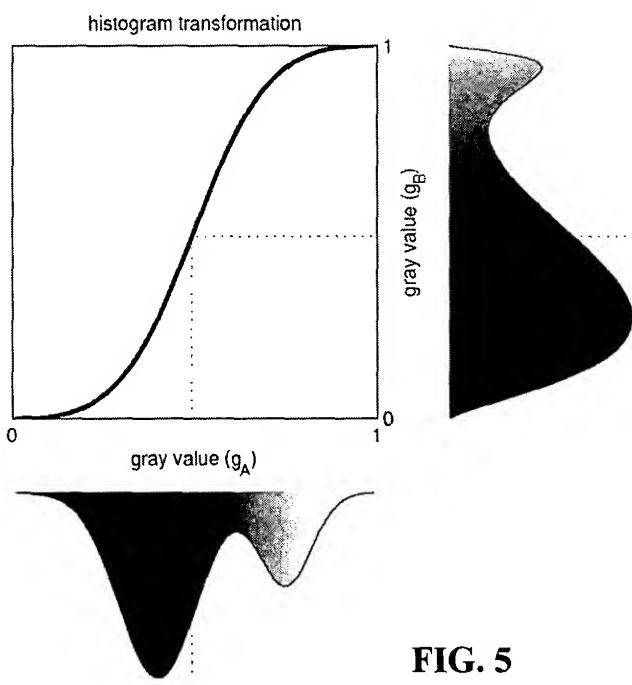
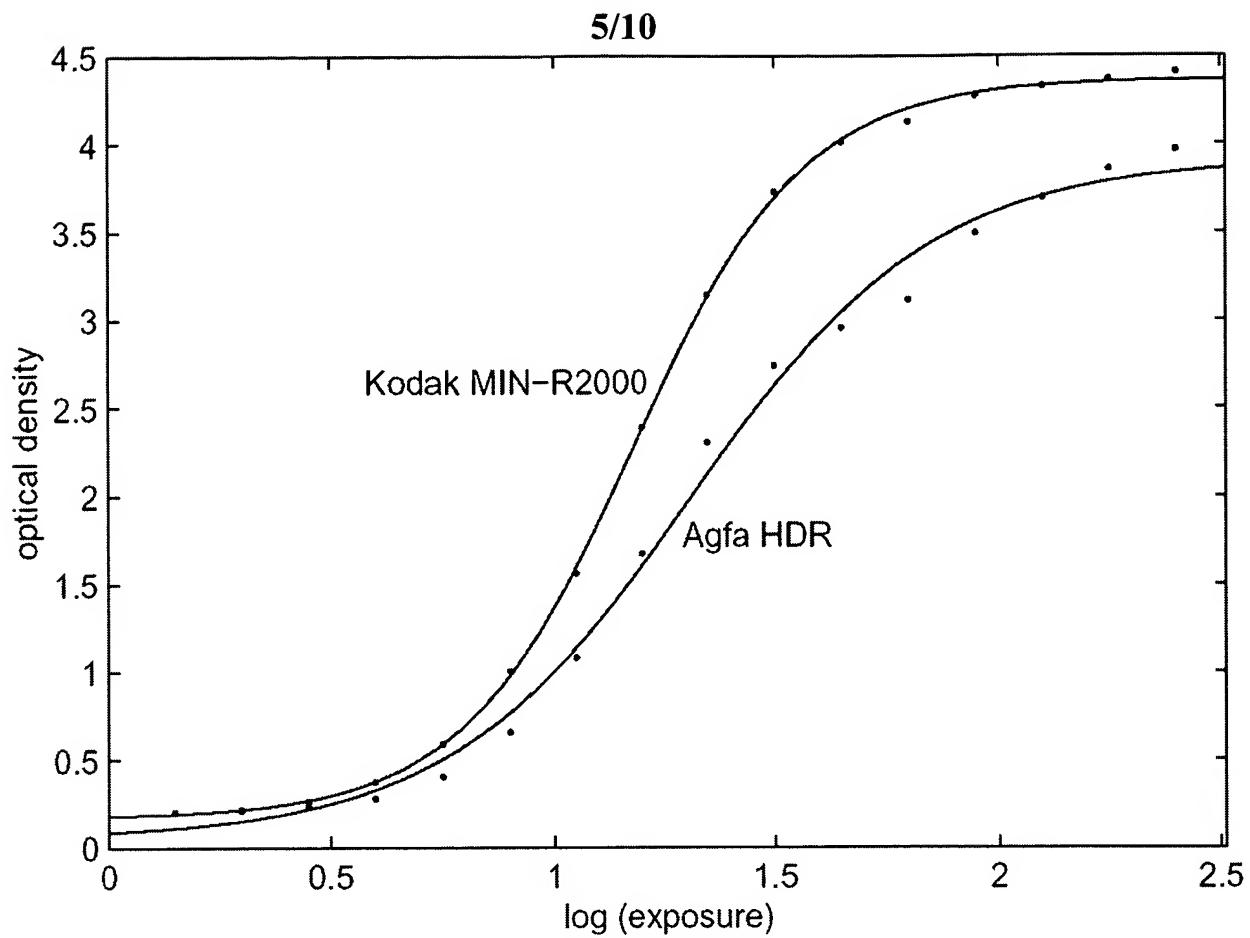
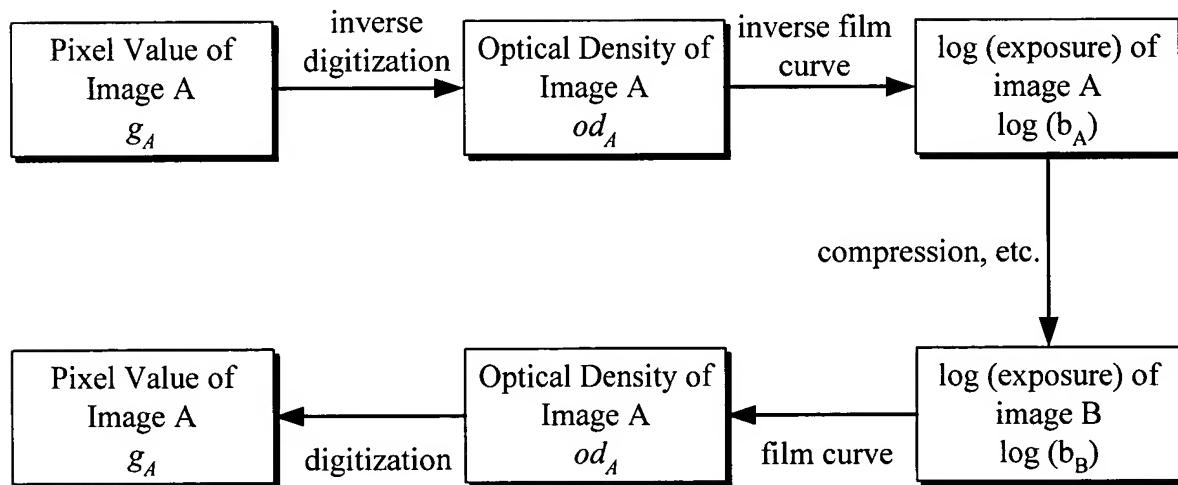


FIG. 5

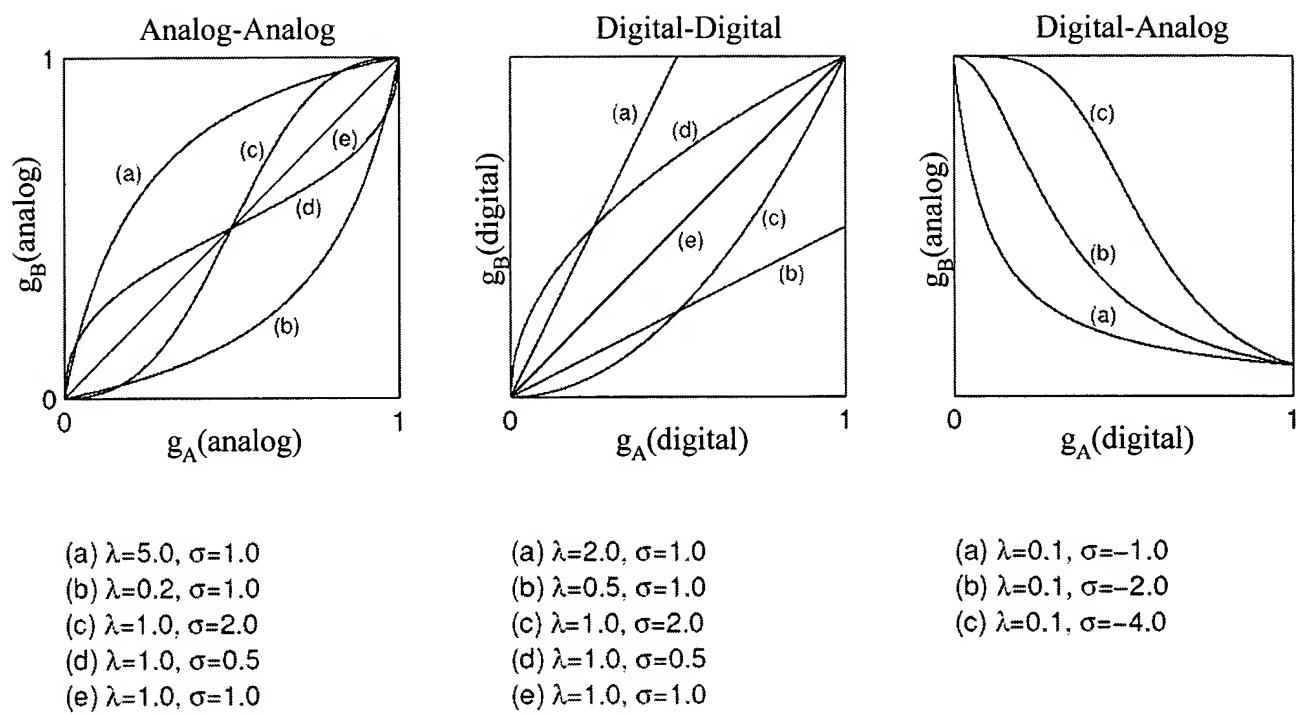


**FIG. 6**

**Analog-Analog**

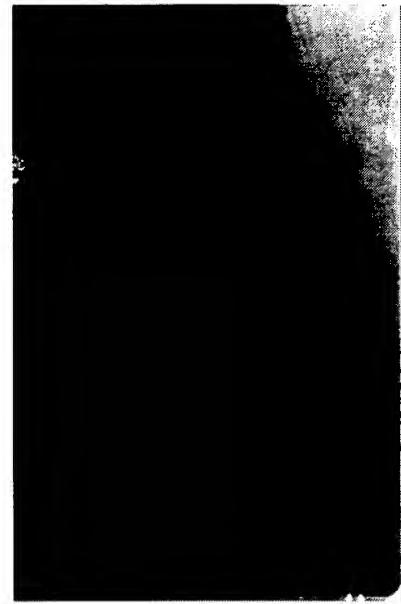
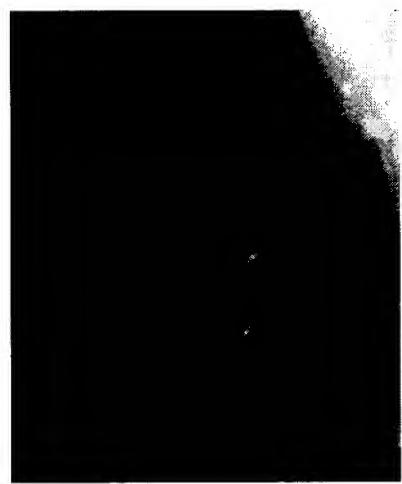


**FIG. 7**

**FIG. 8**

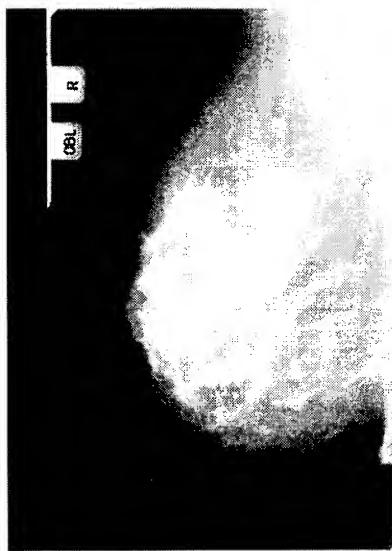
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Digital -> Analog



**FIG. 9**

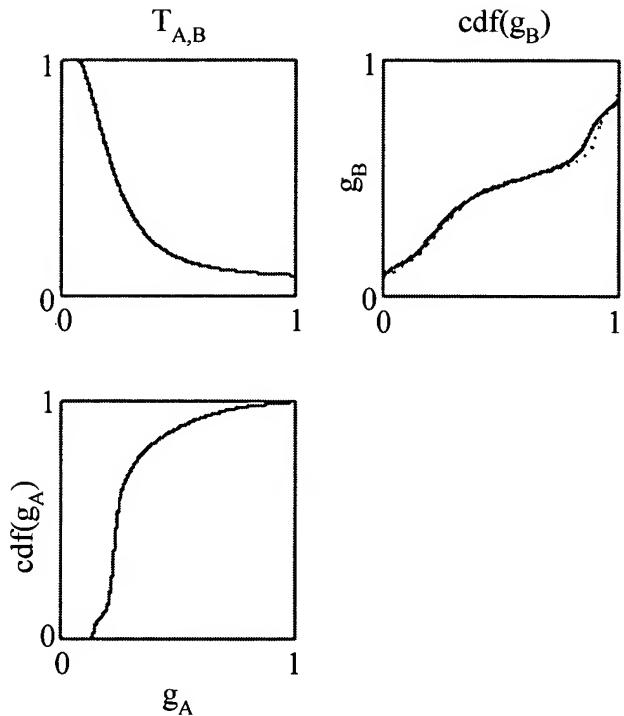
Analog -> Analog



**FIG. 10**

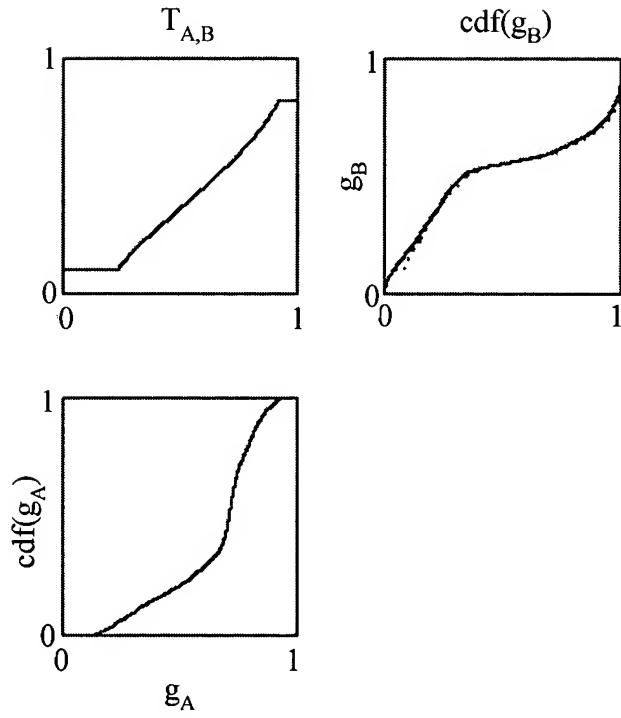
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Digital  $\rightarrow$  Analog

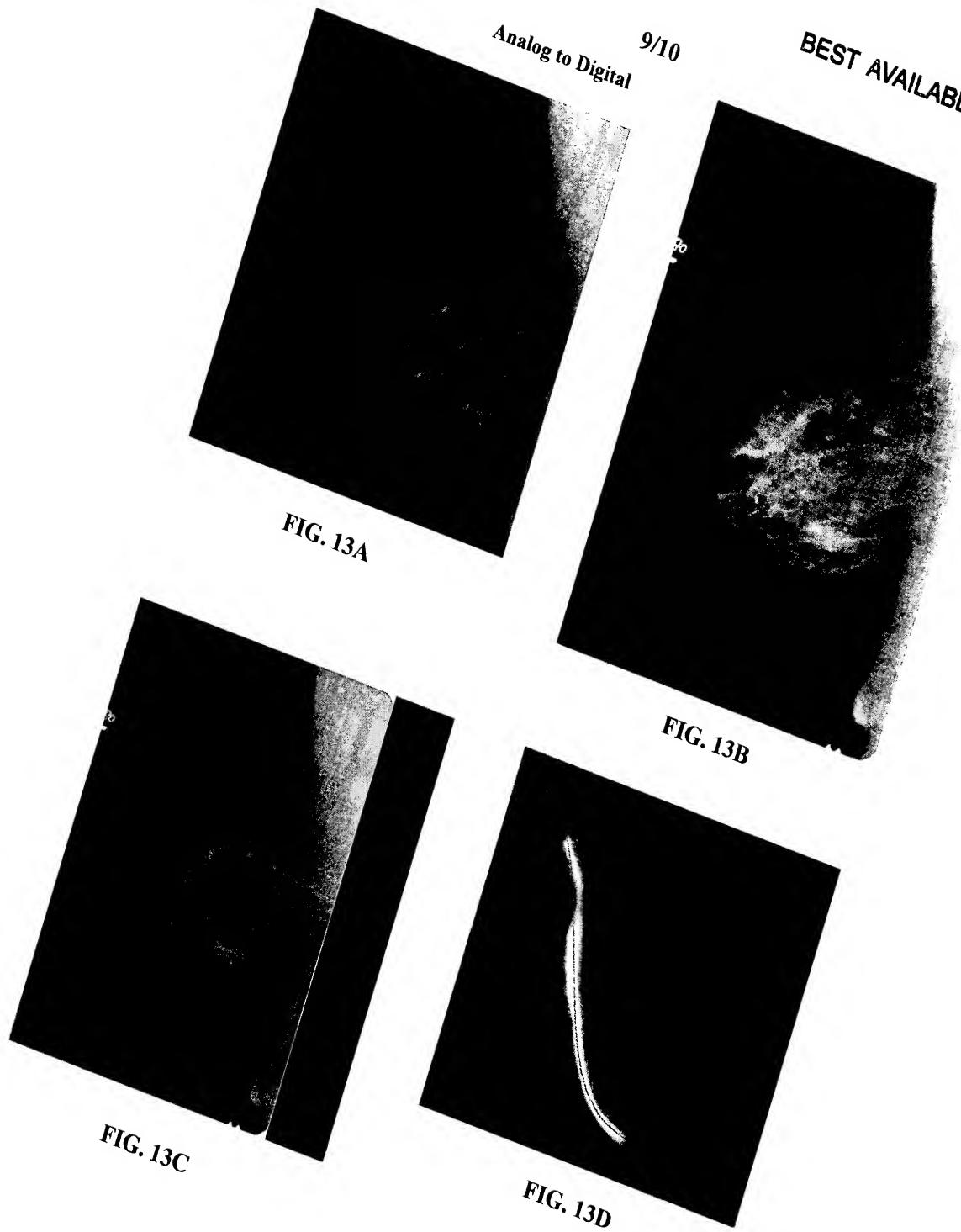


**FIG. 11**

Analog  $\rightarrow$  Analog



**FIG. 12**



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Analog to Analog



FIG. 14A

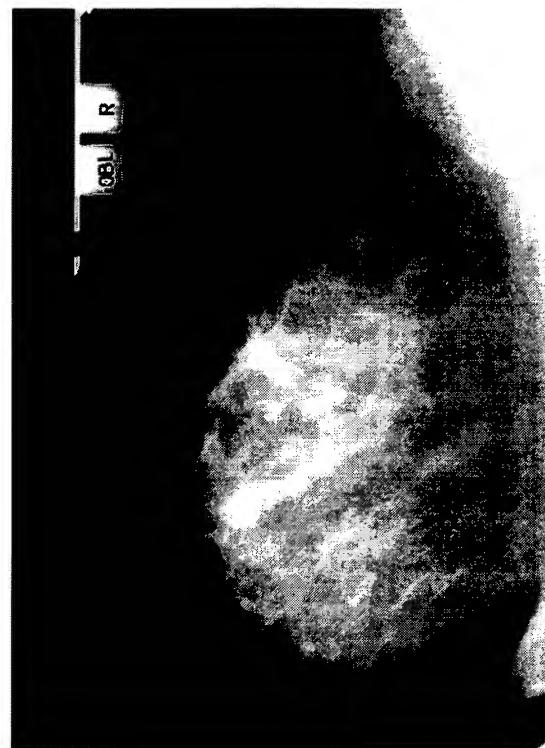


FIG. 14B



FIG. 14D

FIG. 14C